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Cont

laser beam with a pulse frequency of 80 MHz and with a pulse width of 100 fs.

IN THE CLAIMS

Please cancel claims 8-11 without prejudice, and amend claims 1, 4 and 12 as follows (see the attachment for details of the amendment):

82

1. (Twice Amended) A laser scanning microscope comprising:
a pulse laser oscillator configured to oscillate a pulse laser beam to excite a sample;
a scanning mirror configured to scan the pulse laser beam;
5 a photodetector configured to detect light from the sample and output an electric signal;
a pulse generator configured to generate pulses on the basis of the oscillation of the pulse laser beam,
a sampling circuit configured to sample the electric signal
10 in synchronism with each pulse generated by the pulse generator;
and
a memory configured to accumulate data output by the sampling circuit.

83 4. (Twice Amended) The laser scanning microscope according to claim 3, further comprising a pulse generator configured to generate a pulse signal, whose pulse width is smaller than the time period of oscillation of the pulse laser beam, in synchronism with the synchronous signal delayed by the delay circuit, and wherein the sampling circuit samples the electric signal from the photodetector in response to the pulse signal generated by the pulse generator.

12. (Twice Amended) A laser scanning microscope comprising:
a pulse laser oscillator configured to oscillate a pulse laser beam to excite a sample;

By 5 a scanning mirror configured to scan the pulse laser beam;
a photodetector configured to detect light from the sample and output an electric signal;

10 a laser oscillation synchronous signal generating circuit configured to receive a laser oscillation signal from the pulse laser oscillator and generate a laser oscillation synchronous signal;

a delay circuit configured to delay the laser oscillation synchronous signal output from the laser oscillation synchronous signal generating circuit, and configured to output the delayed signal as a trigger signal;

15 a pulse generator configured to generate pulses in synchronism with the trigger signal output from the delay circuit;

By
20 a sampling circuit configured to sample the electric signal output from the photodetector in synchronism with each pulse output from the pulse generator; and

and
a memory configured to accumulate data output by the sampling circuit.
